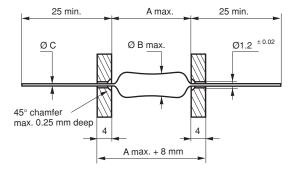


Power Metal Oxide Film Resistors



The "ROP" resistors combine low price with outstanding performance and are therefore suitable for both professional and general purpose applications. These power metal oxide film resistors can favorably be used as a substitute for general purpose cemented or lacquered wirewound resistors, in medium and high ohmic values ranges.

DIMENSIONS in millimeters



FEATURES

- 2W and 4W at 25°C
- NF C 83-210
- · Good overload performance
- High reliability level
- · Small size
- · High electrical insulation
- · Resistance to cleaning solvents
- · Excellent climatic performances
- · Good mechanical strength

DIMEN- SIONS SERIES	A	Ø B	øс	UNIT WEIGHT IN g
ROP2	14.7	4.9	0.8 ± 0.1	1.2
ROP4	25.2	6.9	0.8 ± 0.1	3

TECHNICAL SPECIFICATIONS					
VISHAY SFERNICE SERIES	ROP2	ROP4			
CCTU 04-06A NF C/UTE 83-210	RCP12	RCP13			
Power Rating 25°C	2W	4W			
Critical Resistance	out of nominal ohmic range	out of nominal ohmic range			
Resistance Value Range ± 5% E and Resistance Tolerance	24 1Ω to 56kΩ	4.7Ω to 100kΩ			
Maximum Voltage	350V	500V			
Maximum Operating Temperature	235°C	+ 235°C			

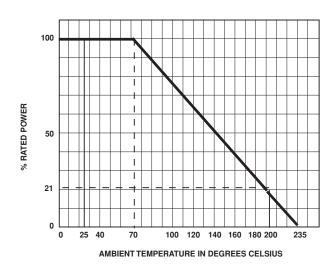
Vishay Sfernice

Power Metal Oxide Film Resistors

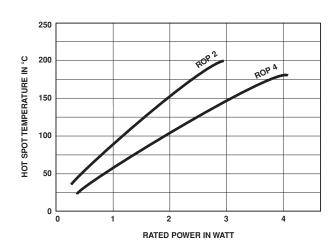


PERFORMANCE							
NF C 83-210 - CECC	REQUIREMENTS	TYPICAL VALUES					
TESTS	CONDITIONS	NF C 83-210	AND DRIFTS				
Short Time Overload	10 Pn during 5 s or 40 V/mn	≤ ± (1% + 0.05)	\pm 0.2% or 0.05 Ω				
Thermal Shock	load at Pr followed by cold temp. exposure 15 min. at – 55°C	-	± 0.2% or 0.05Ω				
Climatic Sequence	10 cycles	≤± (3% + 0.1)	± 0.3% or 0.05Ω				
Load Life	cycle 90'/30' 1000 h at Pr at 25°C	≤± (3% + 0.1)	\pm 1% or 0.05 Ω				
Damp Heat Humidity (Steady State)	56 days with low load	± (3% + 0.1)	± 0.3% or 0.05Ω				
Load Life at max. Category Temperature	200°C 21% of Pn	± (3% + 0.1)	_				
Climatic Sequence	- 55°C + 200°C severity 1-5 cycles	± 3% or 0.05	\pm 3% or 0.05 Ω				

POWER RATING CHART (NF C)



TEMPERATURE RISE



ORDERING INFORMATION								
ROP	2		18k Ω	± 5%				
SERIES	STYLE	SPECIAL DESIGN	OHMIC VALUE	TOLERANCE	PACKAGING			
		Method N° Optional			Optional			

Legal Disclaimer Notice



Vishay

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